

October 1988 Revised March 2000

# **DM74LS298 Quad 2-Port Register Multiplexer with Storage**

## **General Description**

The DM74LS298 is a guad 2-port register. It is the logical equivalent of a quad 2-input multiplexer followed by a quad 4-bit edge-triggered register. A Common Select input selects between two 4-bit input ports (data sources). The selected data is transferred to the output register synchronous with the HIGH-to-LOW transition of the Clock input.

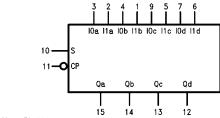
#### **Features**

- Select from two data sources
- Fully edge-triggered operation
- Typical power dissipation of 65 mW

## **Ordering Code:**

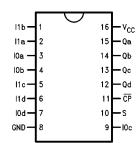
Order Number	Package Number	Package Description
DM74LS298N	N16E	16-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide

## **Logic Symbol**



 $V_{CC} = Pin 16$ GND = Pin 8

## **Connection Diagram**



#### **Pin Descriptions**

Pin Names	Description
S	Common Select Inputs
CP	Clock Pulse Input (Active Falling Edge)
10 <sub>a</sub> , 10 <sub>d</sub>	Source 0 Data Inputs
10 <sub>a</sub> , 10 <sub>d</sub> 11 <sub>a</sub> , 11 <sub>d</sub>	Source 1 Data Inputs
	Flip-Flip Outputs

#### **Truth Table**

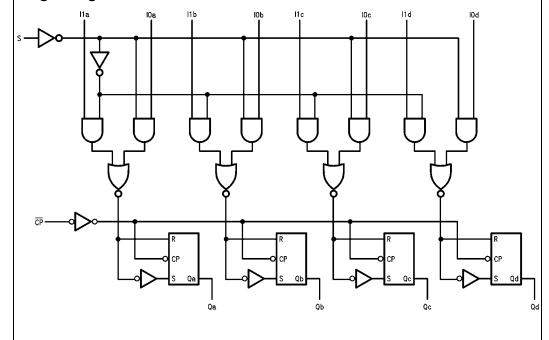
Inputs			Output
S	10 <sub>x</sub>	I1 <sub>x</sub>	Q <sub>x</sub>
I	I	Х	L
1	h	X	Н
h	X	1	L
h	Χ	h	Н

- I = LOW Voltage Level one setup time prior to the HIGH-to-LOW clock
- $h = HIGH\ Voltage\ Level\ one\ setup\ time\ prior\ to\ the\ HIGH-to-LOW\ clock$ transition.
  H = HIGH Voltage Level
- L = LOW Voltage Level
- X = Immaterial

## **Functional Description**

This device is a high speed quad 2-port register. It selects four bits of data from two sources (ports) under the control of a Common Select input (S). The selected data is transferred to the 4-bit output register synchronous with the HIGH-to-LOW transition of the Clock input  $\overline{(CP)}$ . The 4-bit output register is fully edge-triggered. The Data inputs (I<sub>nx</sub>) and Select input (S) need be stable only one setup time prior to the HIGH-to-LOW transition of the clock for predictable operation.

## **Logic Diagram**



## **Absolute Maximum Ratings**(Note 1)

Supply Voltage 7V
Input Voltage 7V
Operating Free Air Temperature Range 0°C to +70°C
Storage Temperature Range -65°C to +150°C

Note 1: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

## **Recommended Operating Conditions**

Symbol	Parameter	Min	Nom	Max	Units	
V <sub>CC</sub>	Supply Voltage	4.75	5	5.25	V	
V <sub>IH</sub>	HIGH Level Input Voltage	2			V	
V <sub>IL</sub>	LOW Level Input Voltage			0.8	V	
I <sub>OH</sub>	HIGH Level Output Current			-0.4	mA	
I <sub>OL</sub>	LOW Level Output Current			8	mA	
T <sub>A</sub>	Free Air Operating Temperature	0		70	°C	
t <sub>S</sub> (H)	Setup Time HIGH or LOW	25			no	
t <sub>S</sub> (L)	S to CP	25			ns	
t <sub>H</sub> (H)	Hold Time HIGH or LOW	0			ns	
t <sub>H</sub> (L)	S to CP	0				
t <sub>S</sub> (H)	Setup Time HIGH or LOW	15			no	
t <sub>S</sub> (L)	I0 <sub>x</sub> or I1 <sub>x</sub> to $\overline{\text{CP}}$	15			ns	
t <sub>H</sub> (H)	Hold Time HIGH or LOW	5.0			no	
t <sub>H</sub> (L)	I0 <sub>x</sub> or I1 <sub>x</sub> to $\overline{\text{CP}}$	5.0			ns	
t <sub>W</sub> (H)	CP Pulse Width HIGH or LOW	20				
t <sub>W</sub> (L)		20			ns	

### **Electrical Characteristics**

over recommended operating free air temperature range (unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ (Note 2)	Max	Units
VI	Input Clamp Voltage	$V_{CC} = Min, I_I = -18 \text{ mA}$			-1.5	V
V <sub>OH</sub>	HIGH Level Output Voltage	$V_{CC} = Min, I_{OH} = Max, V_{IL} = Max$	2.7	3.4		V
V <sub>OL</sub>	LOW Level Output Voltage	$V_{CC} = Min, I_{OL} = Max, V_{IH} = Min$		0.35	0.5	V
		$I_{OL} = 4 \text{ mA}, V_{CC} = \text{Min}$		0.25	0.4	Ĭ
I <sub>I</sub>	Input Current @ Max Input Voltage	$V_{CC} = Max$ , $V_I = 7V$ , $V_I = 10V$			0.1	mA
I <sub>IH</sub>	HIGH Level Input Current	$V_{CC} = Max, V_I = 2.7V$			20	μΑ
I <sub>IL</sub>	LOW Level Input Current	$V_{CC} = Max, V_I = 0.4V$			-0.4	mA
Ios	Short Circuit Output Current	V <sub>CC</sub> = Max (Note 3)	-20		-100	mA
Icc	Supply Current	$V_{CC} = Max, I0_n, I1_n,$ $S = GND, \overline{CP} =  $			21	mA

Note 2: All typicals are at  $V_{CC} = 5V$ ,  $T_A = 25^{\circ}C$ .

Note 3: Not more than one output should be shorted at a time, and the duration should not exceed one second.

## **Switching Characteristics**

at  $V_{CC} = +5V$  and  $T_A = +25^{\circ}C$ 

Symbol	Parameter	$R_L = 2 \text{ k}\Omega, C_L = 15 \text{ pF}$		Units
	Farameter	Min	Max	Units
t <sub>PLH</sub>	Propagation Delay Time			
	LOW-to-HIGH Level Output		25	ns
	CP to Q <sub>n</sub>			
t <sub>PHL</sub>	Propagation Delay Time			
	HIGH-to-LOW Level Output		25	ns
	CP to Q <sub>n</sub>			

#### Physical Dimensions inches (millimeters) unless otherwise noted $\frac{0.740 - 0.780}{(18.80 - 19.81)}$ (2.286) 14 13 12 11 INDEX AREA 0.250 ± 0.010 $(6.350 \pm 0.254)$ PIN NO. 1 1 2 3 4 5 6 7 8 **IDENT** OPTION 01 OPTION 02 0.065 $\frac{0.130 \pm 0.005}{(3.302 \pm 0.127)}$ $\frac{0.060}{(1.524)}$ 4° TYP OPTIONAL 0.300 - 0.320 (1.651)(7.620 - 8.128) $\frac{0.145 - 0.200}{(3.683 - 5.080)}$ 95° ± 5° $\frac{0.008 - 0.016}{(0.203 - 0.406)}$ TYP 90° ± 4° TYP 0.020 $\frac{0.280}{(7.112)}$ (0.508)0.125 - 0.150 (3.175 - 3.810) 0.030 ± 0.015 (0.762 ± 0.381) MIN 0.014 - 0.023 0.100 ± 0.010 (0.325 +0.040

16-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide Package Number N16E

0.050 ± 0.010

 $(1.270 \pm 0.254)$ 

(2.540 **±** 0.254) TYP

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(0.356 - 0.584) TYP

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N16E (REV F)

(8.255 +1.016 -0.381