

Serial Flash

Bridging the NV Memory Divide



IT'S HOT

[What else would you expect
from STMicroelectronics]



High speed SPI bus — small outline package — deep power down mode — hardware or software write protect — just some features of ST's latest range of serial access Flash. Features that will help turn your nomadic application design into a market winner. For ASIC based applications, the serial Flash means lower pin counts and so fewer tracks, smaller boards and lower costs. Deep power down mode, in conjunction with 2.7 and 3.6V operation, means smaller power supplies and longer battery life. And of course, with an SPI bus up to 25MHz you don't even pay a speed penalty.

Available now in sector erase mode, and with page erase mode at the start of 2002, serial access Flash is the optimum trade off between EEPROMs at one end of the memory scale and parallel Flash at the other.

For more information on how you too can bridge the memory divide, go to www.st.com/memory.

SMALL CODE STORAGE

M25PXX Sector Erase
Serial Access
Page Programming
512Kb, 1Mb and 2Mb available
4Mb and 8Mb in development
S08 package
Up to 25MHz SPI bus
2.7 to 3.6 Vcc range

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